

REGISTRATION FORM

Two Days

“National Conference On Hardware Security”

23rd to 24th March 2020

Full Name :

Qualification :

Designation :

Department :

Institution :

Address :

Phone :

E-Mail :

Payment Details :

DD.No / Date :

Bank Name / Place :

NOTE : No TA / DA will be provided.

Signature of Participant

Head of the Institution

Registration Fees :

| | Non ISTE, IETE Member | ISTE, IETE Member |
|----------------------------------------|-----------------------|-------------------|
| Students (UG & PG) & Research Scholars | Rs. 500/- | Rs. 400/- |
| Faculty | Rs. 750/- | Rs. 650/- |
| Industry Participants | Rs. 1250/- | Rs. 1000/- |

The demand draft should be drawn in the favor of “Aarupadai Veedu Institute of Technology”, payable at Chennai.

Important date :

Deadline for Paper Submission : 02nd Mar - 2020

Acceptance Notification : 05th Mar - 2020

Final Manuscript Submission : 10th Mar - 2020

FOR ONLINE PAYMENT

Account Name : Aarupadai Veedu institute of Technology

Account Number : 027805003561

Bank : ICICI BANK LTD

IFSC Code : ICIC0000278

Branch : CHENNAI KILPAUK

Paper Submission Mail ID :

nchs2020.ece@avit.ac.in

The completed Registration form to be sent to

The CONVENERS

Department of Electronics and
Communication Engineering
Aarupadai Veedu Institute of
Technology, Paiyanoor, Chengalpattu
District, Tamil Nadu, India.
Pin Code - 603104.

Ph: +91 7548875171, +91 9445018955

Email: sitadevi.ece@avit.ac.in.
vijay.ece@avit.ac.in.



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Department of
Electronics & Communication Engineering

Organizes

Two Days

“National Conference On Hardware Security”

23rd to 24th March 2020

Conference Theme

Challenges, Opportunities and Strategies involved in
Hardware Security related to VLSI, Signal Processing,
Embedded Systems & Communication Systems



www.avit.ac.in

ABOUT THE INSTITUTION

Aarupadai Veedu Institute of Technology (AVIT) established in the year 1988, is an ambit institution of the Vinayaka Mission's Research Foundation. It is located on OMR, the IT corridor of Chennai. Vinayaka Mission's research foundation is accredited by NAAC. Vinaya Mission's research foundation excel in disciplines like medicine, Dentistry, Paramedical Science, Homeopathy, Engineering, Management and basic sciences and has his wings spread across Tamil Nadu and Pondicherry. AVIT offers undergraduate, postgraduate and research program in all disciplines of Engineering and technology. The institution has adequate number of classrooms and infrastructure for labs with state of the art equipment. The Institution has a well-stocked Library with adequate textbooks, reference books, National and International journals, DVD Library and e-learning facilities in the digital library. The Library has National Digital Library (NDA) access and installed Swayam Prabha DTH Channel facility for Swayam/NPTEL and other MOOC online courses. The college has chapters of various professional societies Like IEEE, IEEE PES, IEEE CSE, IETE, ISTE, IIPE, SAE, etc.,. The management believes in collaborative projects and hence has established with industries and research organizations. The institution has signed Memorandum of Understanding (MoU) with Bosch, Intel, Tech Mahindra, Infosys campus connect, GSS corp Japan, Satsang Research Institute, Baba Clinical and Genomic Research Centre, Chennai, NASSCOM, ICTACT, IYF and many more.

ABOUT ECE DEPARTMENT

The department was established to excel in education and research in Electronics and Communication Engineering (ECE) from the year 1998-it conglomerates academic rigor with a vibrant campus life. The department offers undergraduate, post graduate and Ph.D. programs that provide students with the knowledge and tools to meet the growing technological demands. The broad of courses are taught by the group of highly experienced faculties, who are also researchers in their own areas. Our ECE department has well equipped laboratories coupled with extensive practical work which ensures the students receive a well-rounded education. Keeping this in mind the department has focused on industry defined problems from various Public Sector Units for their student research work. Our ECE department is research intensive which has adequate infrastructure to engage in cutting edge-research activities. ECE department built the fundamental platform of digitization, the impact of microprocessor, motherboard, LED, LCD etc. are various electronic item that influenced economy level of the world to up the notch. 2 Patents (IPR) filed by faculty. More than 120 research papers published in indexed journals.

ABOUT CONFERENCE

The aim of the conference is to provide a global forum of academicians, researchers, industrialists and students engaged in areas of technology to discuss critical issues concerning intricacies of communication engineering that impact the society and also to share and explore new areas of research, development and to discuss emerging issues as well as novel methods. This conference will also provide opportunities for networking and collaboration among the professionals from different disciplines across the country.

CALL FOR PAPERS

Original contributions are solicited for presentation on Emerging Trends in **HARDWARE SECURITY, VLSI, SIGNAL PROCESSING, EMBEDDED SYSTEMS & COMMUNICATION**. The topics of interest include (but are not limited to) the following :

- ☐ Ad Hoc / Sensor Networks ☐ Antenna & Wave Propagation
- ☐ ASIC Design / FPGA Implementation
- ☐ Hardware Trojan ☐ Network Security and Cryptography
- ☐ Trusted Hardware Module ☐ Biomedical Imaging
- ☐ Data Mining ☐ Embedded/RTOS Applications
- ☐ Image/Video Processing ☐ Internet of Things (IoT)
- ☐ Low Power VLSI/VLSI Testing ☐ Mobile/ Satellite Communications
- ☐ Nano Electronics ☐ Network Security and cryptography
- ☐ Pattern Recognition ☐ RF Design & Microwave Communication
- ☐ Smart Grid Technologies ☐ Soft Computing Techniques
- ☐ VLSI/Reconfigurable Architectures ☐ Renewable Energy
- ☐ Wavelet Transform & Applications ☐ Wireless Communications

The full paper should be in accordance with IEEE guidelines. (Not exceeding 6 pages)

PAPER FORMAT & PLAGIARISM POLICY:

Submissions must include title, abstract, author affiliation with the email address and keywords. Submitted papers should be maximum of 6 pages in length in standard IEEE conference format. The proceedings of the conference will be published with ISBN number. All submitted papers will be subjected to a "similarity test" by registered plagiarism Software. Papers achieving a minimal similarity index i.e., less than 30% will be examined, and those are deemed unacceptable will be rejected/ withdrawn.

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Mr. J. Vijay, Asst. Prof. (Gr-II)/ECE

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